

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L5	1881	361/111.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/08 10:53
L6	365	365/185.04.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/08 10:54
L7	668	257/173.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/08 10:54
L8	1243	257/529.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/08 10:55
L9	4104	I5 or I6 or I7 or I8	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/08 10:55
L10	929	chip and fuse and ((electrostatic with protection) or ESD)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2009/06/08 10:55
L11	74	I9 and L10	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2009/06/08 10:55
L12	13	I11 and (input with pad)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2009/06/08 10:57

L13	0	semiconductor with circuit with inner with electrostatic with protection with fuse with disconnect with package with pair with transistors. clms.	US-PGPUB	OR	OFF	2009/06/08 10:59
S1	477	chip and fuse and ((electrostatic with protection) or ESD)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/07 16:19
S2	117	(chip and fuse and ((electrostatic with protection) or ESD)) and (input with pad) and (input with circuit)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/14 17:08
S3	2	((chip and fuse and ((electrostatic with protection) or ESD)) and (input with pad) and (input with circuit)) and (fuse with wiring with electrostatic)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/14 17:10
S4	16	((chip and fuse and ((electrostatic with protection) or ESD)) and (input with pad) and (input with circuit)) and (fuse with electrostatic)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/09 16:02
S5	16	((chip and fuse and ((electrostatic with protection) or ESD)) and (input with pad) and (input with circuit)) and (fuse with electrostatic)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/09 16:02
S6	0	S5 and ((trim or trimming) with laser)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/09 16:03
S7	512	chip and fuse and ((electrostatic with protection) or ESD)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/09 16:03

S8	12	S7 and ((trim or trimming) with laser)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/09 16:13
S9	0	S8 and (first with (protection adj element)) and (second with (protection adj element))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/09 16:07
S10	2	S7 and (first with (protection adj element)) and (second with (protection adj element))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/09 16:07
S11	7	S7 and (first adj chip) and (second adj chip)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/09 16:13
S12	5	(input with circuit with pad) and (wiring with input with pad) and (first with electrostatic with protection) and (second with electrostatic with protection) and fuse	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/07 16:28
S13	16	(input with circuit with pad) and (first with electrostatic with protection) and (second with electrostatic with protection) and fuse	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/07 16:37
S14	2	(second with electrostatic with protection) with fuse with wiring	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/07 16:38
S15	6	(second with electrostatic with protection) with fuse	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/07 16:38

S16	5	(US-20020109950-\$ or US-20020140107-\$).did. or (US-6154388-\$ or US-6430016-\$ or US-6556409-\$).did.	US-PGPUB; USPAT	OR	ON	2008/06/08 16:09
S17	1	S16 and (capacitor with transistor)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/06/08 16:10
S18	1	10/676071	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/12/18 14:34
S19	605	(source adj drain adj capacitance)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/12/18 14:54
S20	0	(source adj drain adj capacitance) and CMOS and ESD and fuse	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/12/18 14:55
S21	8	(source adj drain adj capacitance) and CMOS and ESD	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/12/18 14:55
S22	460	(source with drain with capacit\$4) and CMOS and ESD	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/12/18 15:00
S23	53	(source with drain with capacit\$4) and CMOS and ESD and fuse	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/12/18 15:00
S24	10	(source with drain with capacit\$4) and CMOS and ESD and fuse and (input with pad)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/12/18 15:00

S25	0	(source with drain with capacit\$4 with fuse) and CMOS and ESD and (input with pad)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/12/18 22:16
S26	11	(capacit\$4 with fuse) and CMOS and ESD and (input with pad)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/12/18 22:16
S27	0	(capacitor with fuse with ESD with transistor) and (input with pad)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/12/18 22:28
S28	1	(capacitor with fuse with ESD with transistor)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/12/18 22:28
S29	9206	transistor with form with capacitor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/12/18 22:32
S30	2107	transistor with acts with capacitor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/12/18 22:33
S31	10655	transistor adj capacitor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/12/18 22:33
S32	14	transistor adj (form) adj capacitor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/12/18 22:35
S33	0	S32 and (fuse with ESD)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/12/18 22:36
S34	0	S32 and fuse and ESD	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/12/18 22:36

S35	0	S32 and fuse	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/12/18 22:36
S36	12	(US-20020109950-\$ or US-20020140107-\$ or US-20040135229-\$). did. or (US-6154388-\$ or US-6430016-\$ or US-6556409-\$ or US-6700433-\$ or US- 5530612-\$ or US- 5345422-\$ or US- 6243283-\$ or US- 4716302-\$ or US- 6239651-\$).did.	US-PGPUB; USPAT	OR	ON	2008/12/18 22:44
S37	8	S36 and fuse and ESD	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/12/18 22:44
S38	2	S36 and (fuse with ESD)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/12/18 22:44
S39	6	S36 and (fuse with (ESD or protection))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/12/18 22:45
S40	2	S36 and (fuse with (ESD or protection)) and (transistor with capacitor)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/12/18 22:47
S41	5	S36 and (transistor with capacitor)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/12/18 22:48
S42	4	S36 and (transistor with capacitor) and fuse	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/12/18 22:51

S43	3	S41 and fuse and ESD	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/12/18 22:54
S44	2306	MOS with source with drain with (connected or shorted) with capacitor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/12/18 23:06
S45	560	source with drain with (connected or shorted) with together with capacitor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/12/18 23:07
S46	0	S45 and (fuse with ESD)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/12/18 23:10
S47	0	S45 and (fuse with (ESD or protection))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/12/18 23:11
S48	30	S45 and fuse	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/12/18 23:11
S49	0	S25 and electrostatic	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/12/18 23:12
S50	0	S48 and electrostatic	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/12/18 23:12
S51	0	S45 and fuse and electrostatic	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/12/18 23:13
S52	6	S45 and fuse and protection	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/12/18 23:13

**6/8/09 11:00:19 AM**

**C:\Documents and Settings\EWojciechowicz\My Documents\EAST\Workspaces  
\10676071.wsp**